OC5050

Features

- Low Aging rate
- SC Cut
- Compact size
- Environmentally friendly product

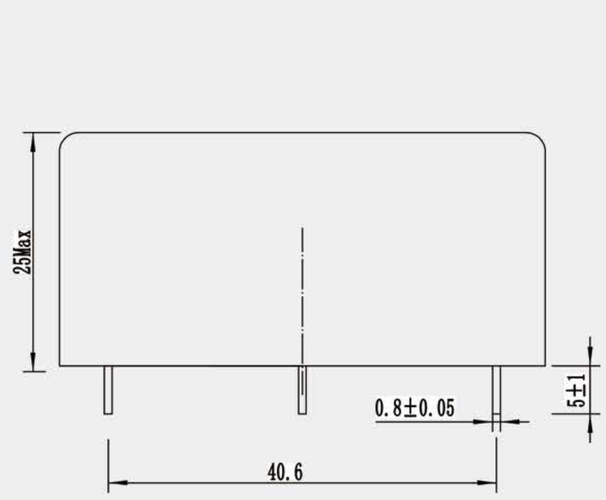
Application

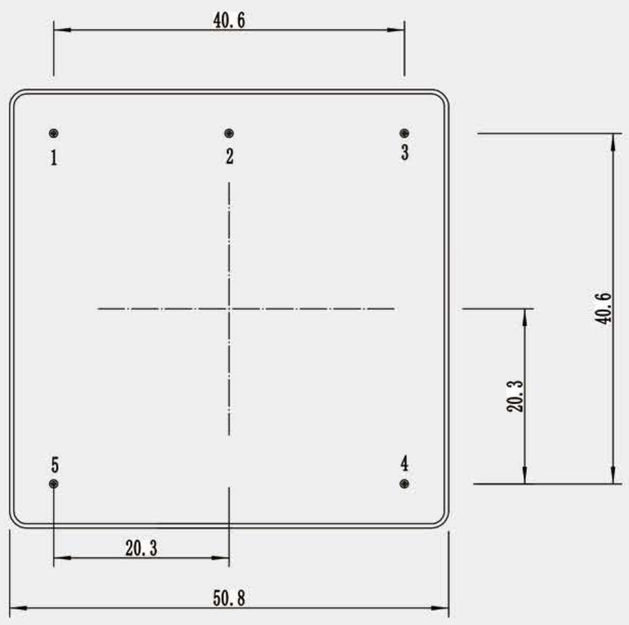
- Communication Network
- Clock Synchronization
- Signal Acquisition and Monitoring
- Rf Microwave
- Military Equipment

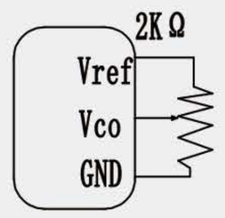
Electrical Specification

Model		OC5050		
Frequency Range		1.000MHz~160.000MHz		
Nominal Frequency (MHz)		4.096 5 8.192 10 16.384 20		
Frequency Tolerance		< ± 0.03PPM (Relative center control voltage) at 25° C		
Supply Voltage (V)		A:+3.3VDC ± 10%; B:+5.0VDC ± 10%; C:+12.0VDC ± 10%		
Supply	Warm–up	5W Max.		
Consumption	Steady State	2W Max. (at 25° C)		
Output Waveform		A: TTL 15pF B: TTL 50pF	C: CMOS 15pF C: CMOS 50pF	G: Sine Wave
Output Symmetry		45%~55%		
Low Jitter				
Frequency Stability relative to	Temperature			
	Input Voltage	< ± 1 × 10ˆ–9 (VDD ± 5%)		
	Load	< ± 1 × 10ˆ–9Max. (Load ± 5%)		
	Start–Up Time	<7min(Fluctuation Not Exceeding ± 10–8 × F0, F0 Is Frequency After One Hour Work).		
Rise time/Fall time		10nS Max.		
Output Level	"0"	0.4V Max.	10%VDD	>0dBm//50 Ω
	"1"	2.4V Min	90%VDD	
Storage Temperature		–40° C~+100° C		
Frequency Aging (After 30 days at +25℃)		B: ± 5 × 1010ˆ–9 /Day / ± 5 × 1010ˆ–7/ Year C: ± 1 × 1010ˆ–9/Day / ± 1 × 1010ˆ–7/ Year		D: ± 5 × 1010ˆ–10/Day / ± 5 × 1010ˆ–8/ Year
Phase noise		100Hz	1KHz	10KHz
		–120dBc/Hz	–145dBc/Hz	–155dBc/Hz
Internal Reference Voltage		4V ± 0.08(VDD=5V)	8V ± 0.16(VDD=12V)	
Slope / Linearity		Just/ ± 10%		

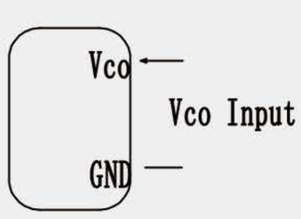
Drawing







Variable resistor



An external voltage

Pin	Functionality
#1	Control vge/suspension
#2	Reference voltage/suspension
#3	Output
#4	Ground
#5	Power