

Features

- Low Aging rate
- AT Cut or SC Cut
- Compact size
- Environmentally friendly product

Application

- Communication Network
- Clock Synchronization
- Signal Acquisition and Monitoring
- Military Equipment



Electrical Specification

Model		OC2020					
Frequency Range		1.000MHz~100.000MHz					
Nominal Frequency (MHz)		4.096	5	8.192	10	16.384	20
Frequency Tolerance		$\pm 0.1\text{PPM}$ (Relative center control voltage) at 25° C					
Supply Voltage (V)		A:+3.3VDC $\pm 10\%$; B:+5.0VDC $\pm 10\%$; C:+12.0VDC $\pm 10\%$					
Supply	Warm-up	3.6W Max.					
Consumption	Steady State	1.5W Max. (at 25° C)					
Output Waveform		A: TTL 15pF	B: TTL 50pF	C: CMOS 15pF	D: CMOS 50pF	G: Sine Wave	
Output Symmetry		45%~55%					
Low Jitter							
Frequency	Input Voltage	$\pm 9 \times 10^{-9}$(VDD $\pm 5\%$).					
Stability	Load	$\pm 5 \times 10^{-9}$Max. (Load $\pm 5\%$)					
relative to	Start-Up Time	<math>< 7\text{min}</math>(Fluctuation Not Exceeding $\pm 10^{-8} \times F_0$, F_0 Is Frequency After One Hour Work).					
Rise time/Fall time		10nS Max.					
Output Level	"0"	0.4V Max.	10%VDD		>0dBm//50 Ω		
	"1"	2.4V Min	90%VDD				
Storage Temperature		-40° C~+100° C					
Frequency Aging (After 30 days at +25°C)		B: $\pm 5 \times 10^{-9}/\text{Day}$ / $\pm 5 \times 10^{-7} / \text{Year}$ C: $\pm 1 \times 10^{-9} / \text{Day}$ / $\pm 1 \times 10^{-7} / \text{Year}$					
Phase noise		100Hz	1KHz		10KHz		
		-120dBc/Hz	-145dBc/Hz		-155dBc/Hz		
Internal Reference Voltage		4V ± 0.08 (VDD=5V)		8V ± 0.16 (VDD=12V)			
Slope / Linearity		Just/ $\pm 10\%$					

Drawing

